

 145 Adams Avenue, Hauppauge, NY 11788, USA

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Notification Date:

July 25, 2024

Product / Process Change Notice

Devices affected:

The listed P-Channel MOSFET wafers.

Extent of change:

The wafer backside wet etch was changed from a manual to automated process.

Reason for change:

In order to enhance wafer processing and improve yields, the backside wet etch process has been changed from a manual to automated process. Electrical performance of the wafers are unchanged.

Effect of change:

This change does not affect the fit, form or function of the devices.

Qualification data:

Stress Test	Abrv	Test Condition	Sample Size	Readout Time	Result (F/SS)	Status
High Temperature Reserve Bias (JESD22 A-108)	HTRB	150°C, 100% Rated V _{DS}	78	T = 0 hr	0/78	Pass
				T = 168 hr	0/78	
				T = 500 hr	0/78	
				T = 1000 hr	0/78	
High Temperature Gate Bias (JESD22 A-108)	HTGBN	150 [°] C, 100% Rated V _{GS} , OFF	78	T = 0 hr	0/78	Pass
				T = 168 hr	0/78	
				T = 500 hr	0/78	
				T = 1000 hr	0/78	
	HTGBP	150°C, 100% Rated V _{GS} , ON	78	T = 0 hr	0/78	Pass
				T = 168 hr	0/78	
				T = 500 hr	0/78	
				T = 1000 hr	0/78	

Earliest effective date of change:

October 25, 2024

Sample availability:

Please contact your salesperson or manufacturer's representative for samples.



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Part Numbers Affected:

CP802-CEN1482-CM	CP776-CDM305P-WS105
CP802-CEN1482-WN	CP776-CEN1458-WN
CP802-CWDM3011P-CM	CP776-CEN1458-WS105
CP802-CWDM3011P-CT	CP776-CWDM305P-CM
CP802-CWDM3011P-WN	CP776-CWDM305P-CM20
CP805-CXDM4060P-CM	CP776-CWDM305P-CT
CP805-CXDM4060P-CT	CP776-CWDM305P-WN
CP805-CXDM4060P-WN	CP798X-CPDM302PH-CT
CP806-CEN1474-WN	CP798X-CPDM302PH-WN
CP806-CEN1474-WR	

As per JEDEC standard JESD46, Customer Notification of Product/Process Changes by Solid-State Suppliers, a lack of acknowledgement of a PCN within thirty (30) days constitutes acceptance of the change.

The undersigned acknowledges and accepts Central Semiconductor's Product/Process Change Notification (PCN).

Company Name:	
Address:	
Printed Name:	
Title:	
Signature:	
Date:	